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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Goodnow et al.

Serial No.: 10/707,323

Filed: December 5, 2003

Title: Method of Selectively Building Redundant
Logic Structures to Improve Fault Tolerance

Group Art Unit: 2825

Examiner: Nghia M. Doan

Attorney Docket No.: BUR920030125US1
(02016-00168)☒ CERTIFICATION OF FACSIMILE TRANSMISSIONI hereby certify that this correspondence is being facsimile
transmitted to the United States Patent and Trademark Office,
Fax No. 571-273-8300 on the date shown below.Norman S. Yi 8/1/06
Norman S. Yi DateCommissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

August 1, 2006

Supplemental Amendment Transmittal

Transmitted herewith for filing is a Supplemental Amendment to Second Office Action
on the Merits with regard to the above-identified Application.

- ☒ No additional fee is required.
- ☐ The fee has been calculated as shown below:

P248-10/03

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